

A few words on Jorway timing



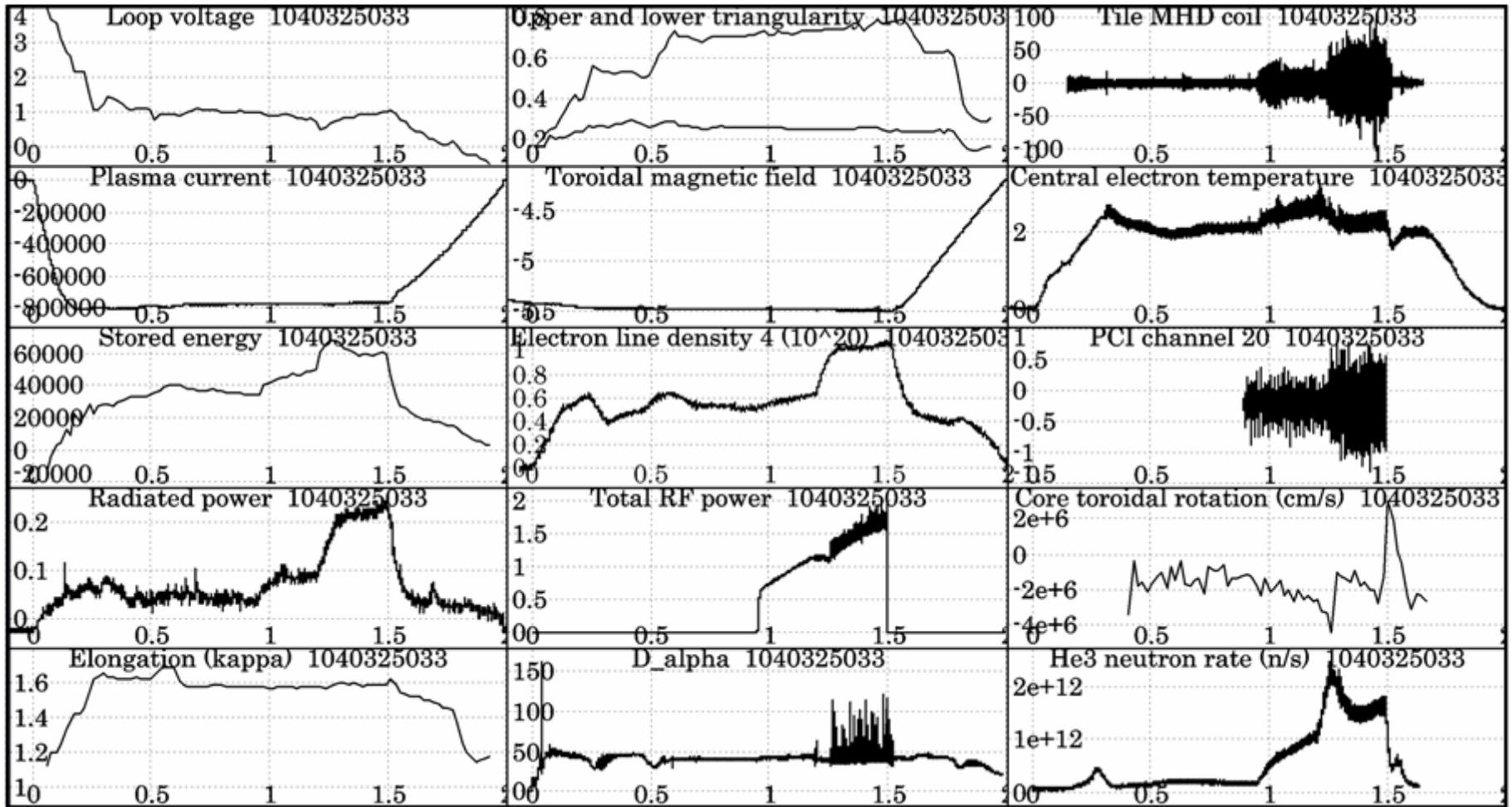
Basse, Bosco, Greenwald, Snipes, Stillerman

MIT Plasma Science and Fusion Center

- At Martin's analysis of ELMy data from run 1040325 it was noted that PCI fluctuations preceded the D-alpha light signal.
- Martin suggested Jorway issue, verified by Josh and Joe Bosco.
- *Problem: There is both a decoder and a Jorway 1 MHz clock. If the Jorway uses the internal clock, there is an offset in triggering of data acquisition boards.*
- *Solution: Set-up all Jorway modules to use the external clock from the decoder.*
- Joe Bosco has checked all Jorway modules he knows of, whether they use an internal or external clock is written in the OPS bulletin board note sent 5-27-2004.

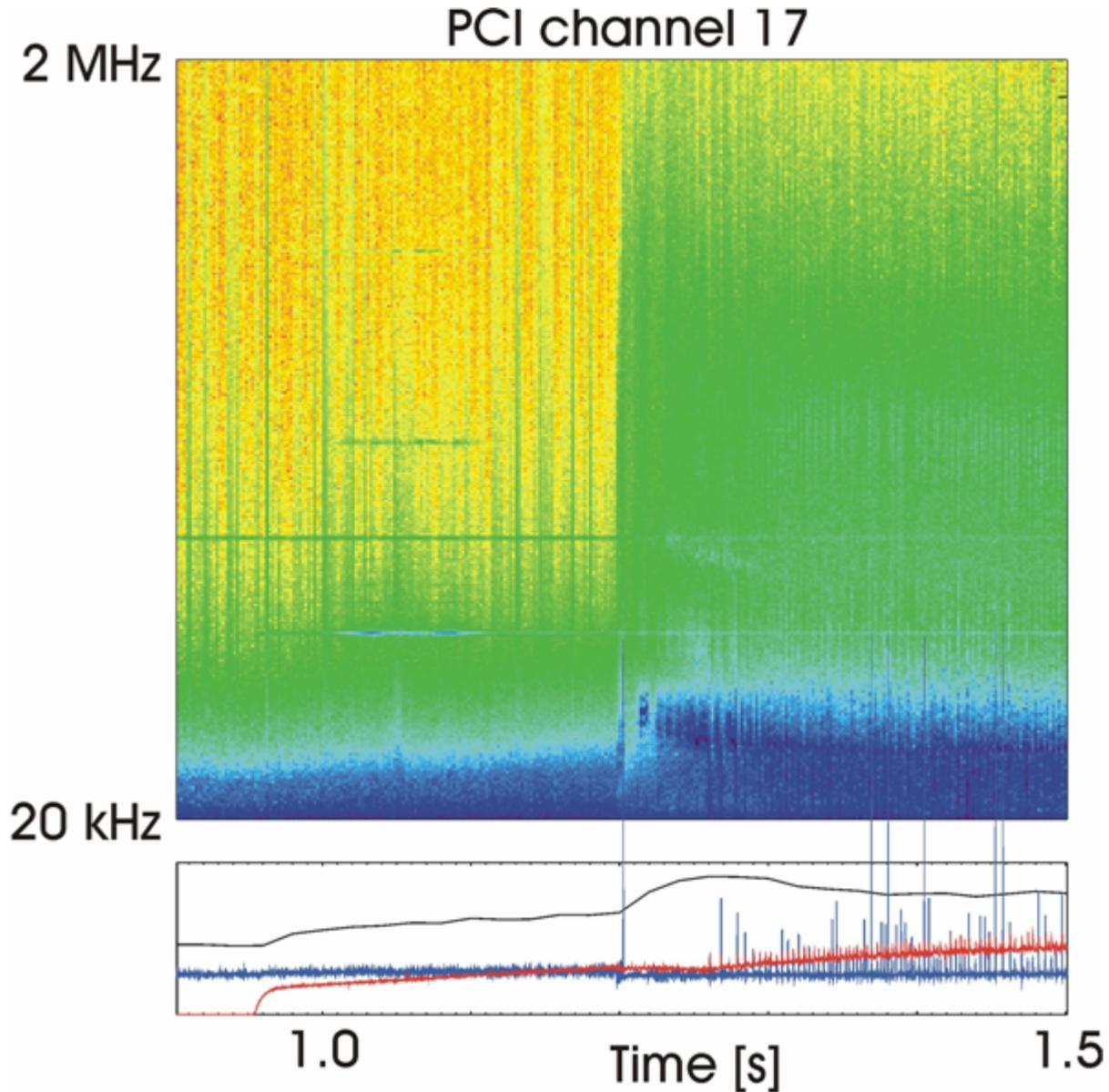


Shot 1040325033

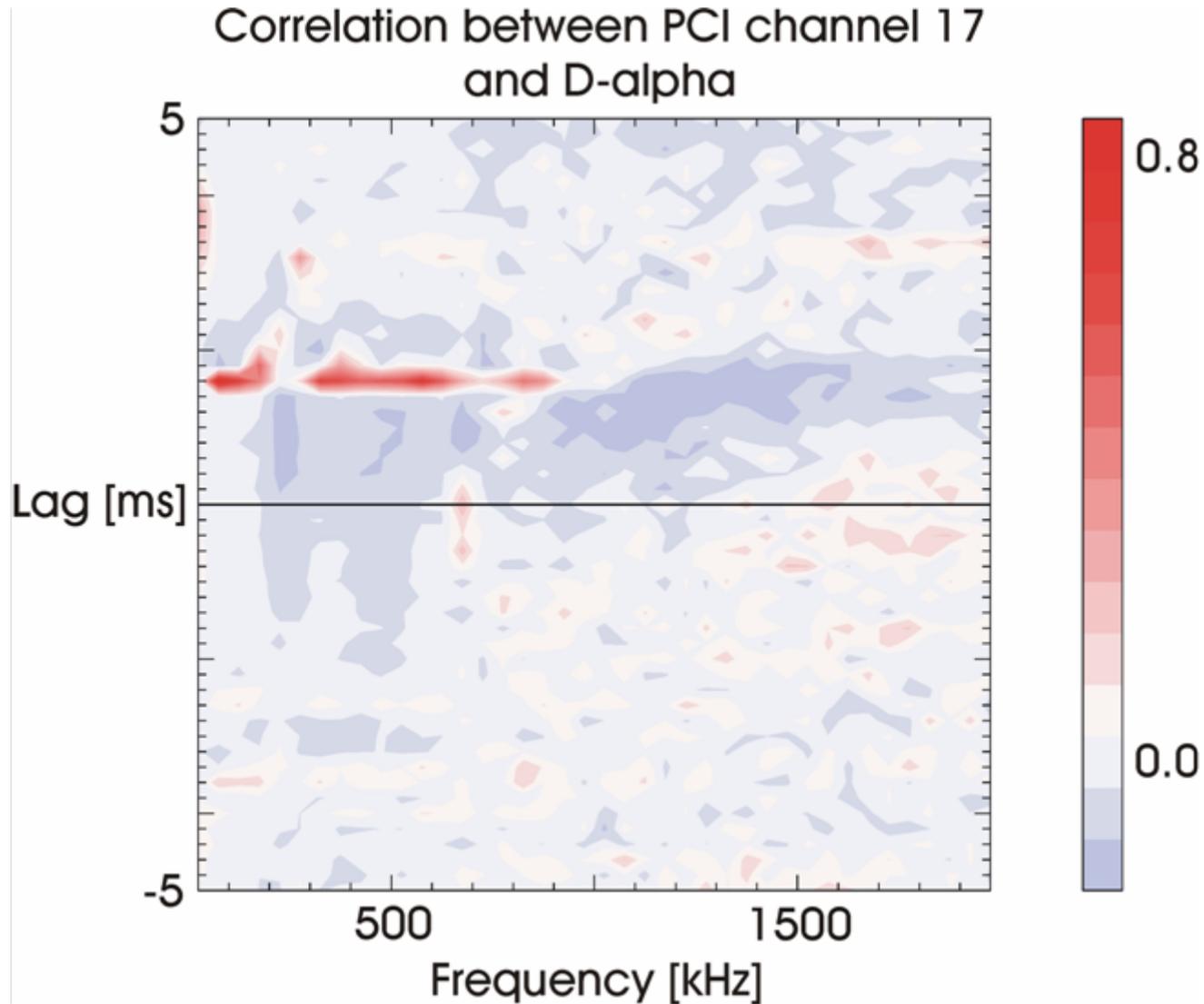


MP#384 - Experiments on Plasma Flows and the L-H Threshold (LaBombard, Greenwald et al.).

PCI spectrogram, core channel



D-alpha/PCI correlations



PCI sees ELMs 1.6 ms before D-alpha.